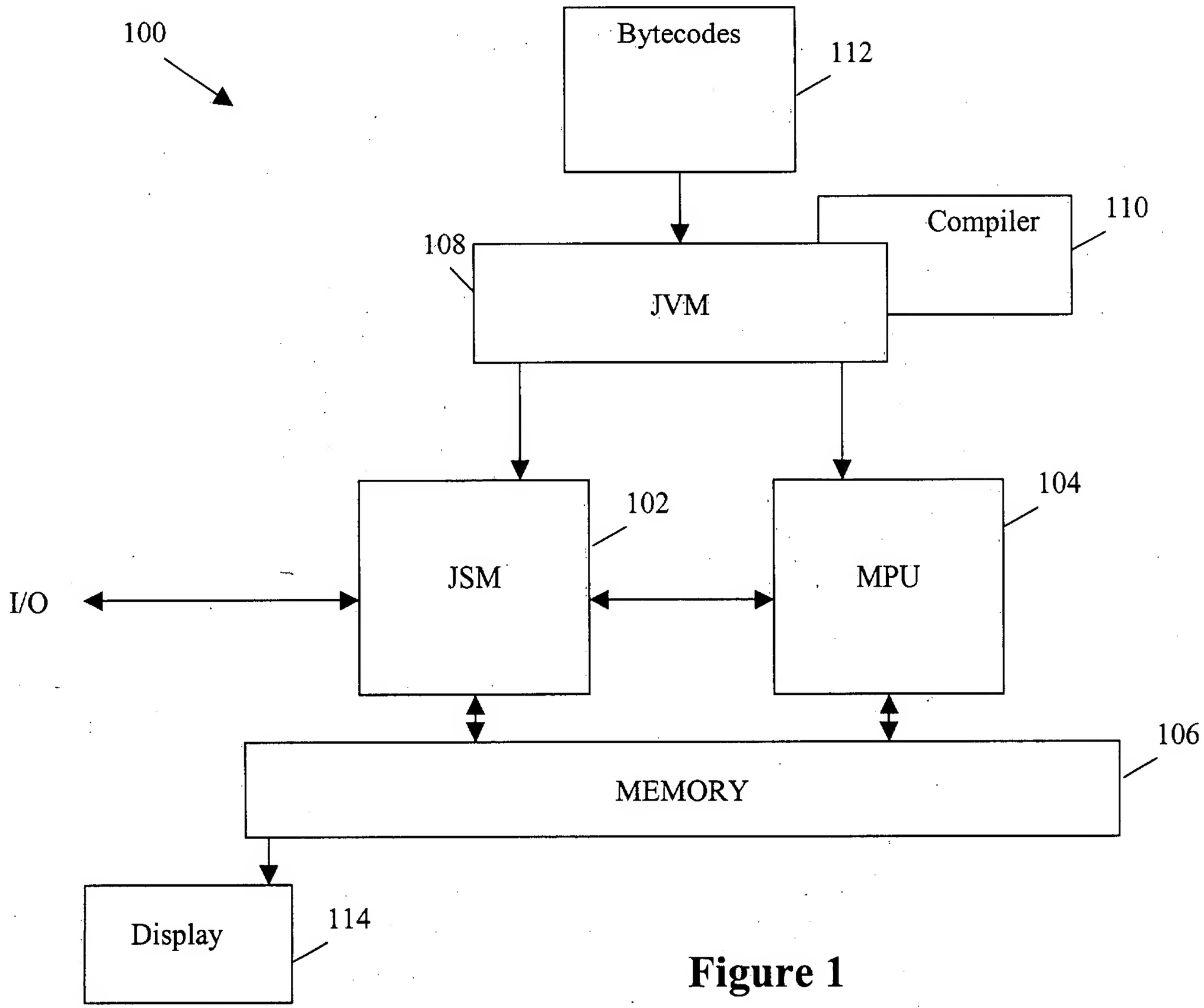
100
Bytecodes
112
Compiler
110
108
JVM
102
JSM
104
MPU
I/O
106
MEMORY
Display
114

Figure 1

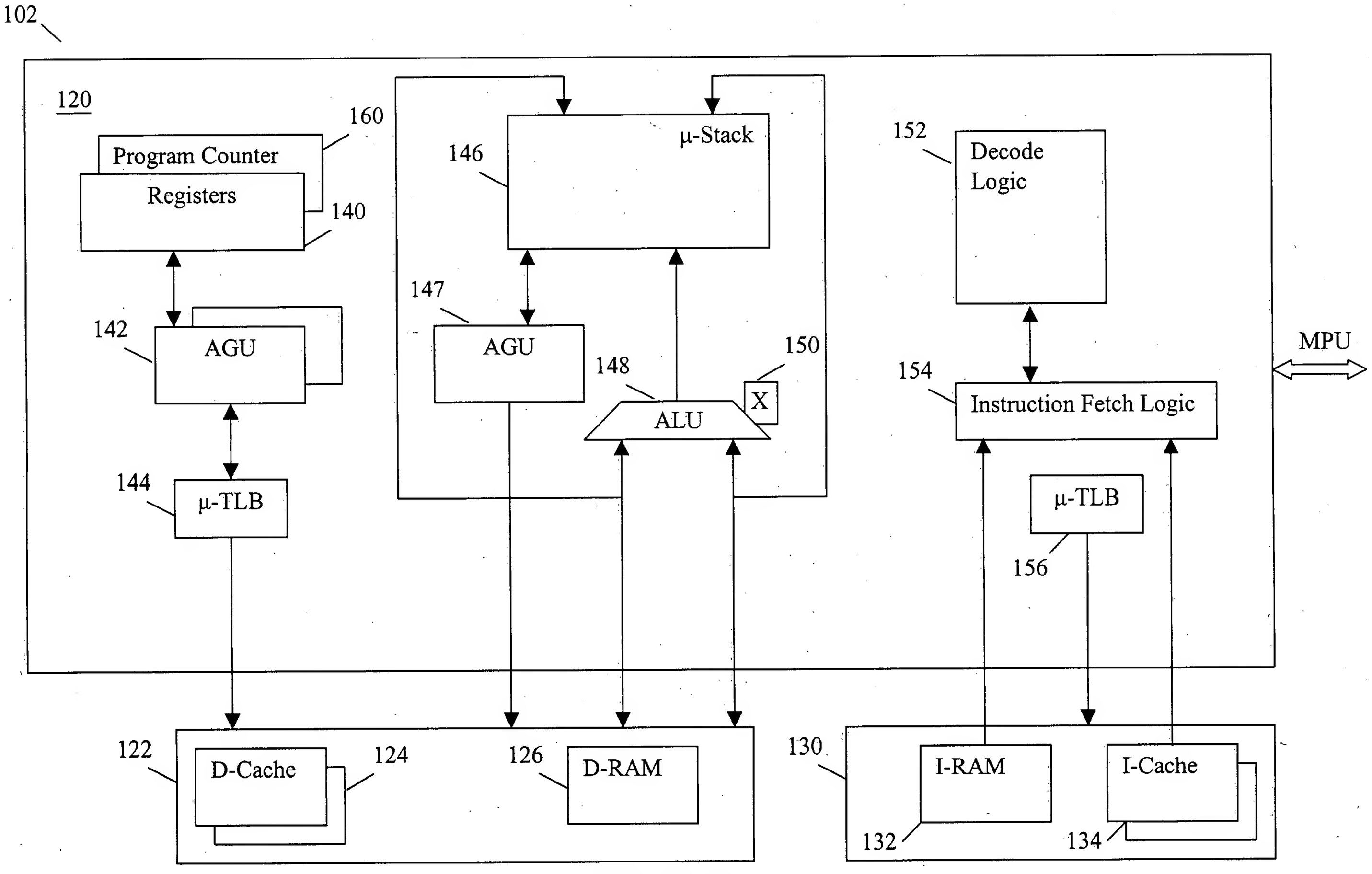
102
120
160
Program Counter
Registers
140
142
AGU
144
μ-TLB
146
μ-Stack
147
AGU
148
ALU
150
X
152
Decode Logic
154
Instruction Fetch Logic
156
μ-TLB
MPU
122
D-Cache
124
126
D-RAM
130
I-RAM
132
I-Cache
134

Figure 2

140

R0	General Purpose (GP)
R1	General Purpose (GP)
R2	General Purpose (GP)
R3	General Purpose (GP)
R4	Program Counter (PC)
R5	General Purpose/Local Variable Pointer (LV)
R6	Stack Pointer (SP)
R7	Top of Stack (ToS)
R8	General Purpose/Address Index 0 (AI0)
R9	General Purpose/Address Index 1 (AI1)
R10	General Purpose (GP)
R11	General Purpose (GP)
R12	Micro-program counter (micro-PC)
R13	General Purpose (GP)
R14	General Purpose/Indirect Register Index (IRI)
R15	Status and Control (ST)

Figure 3

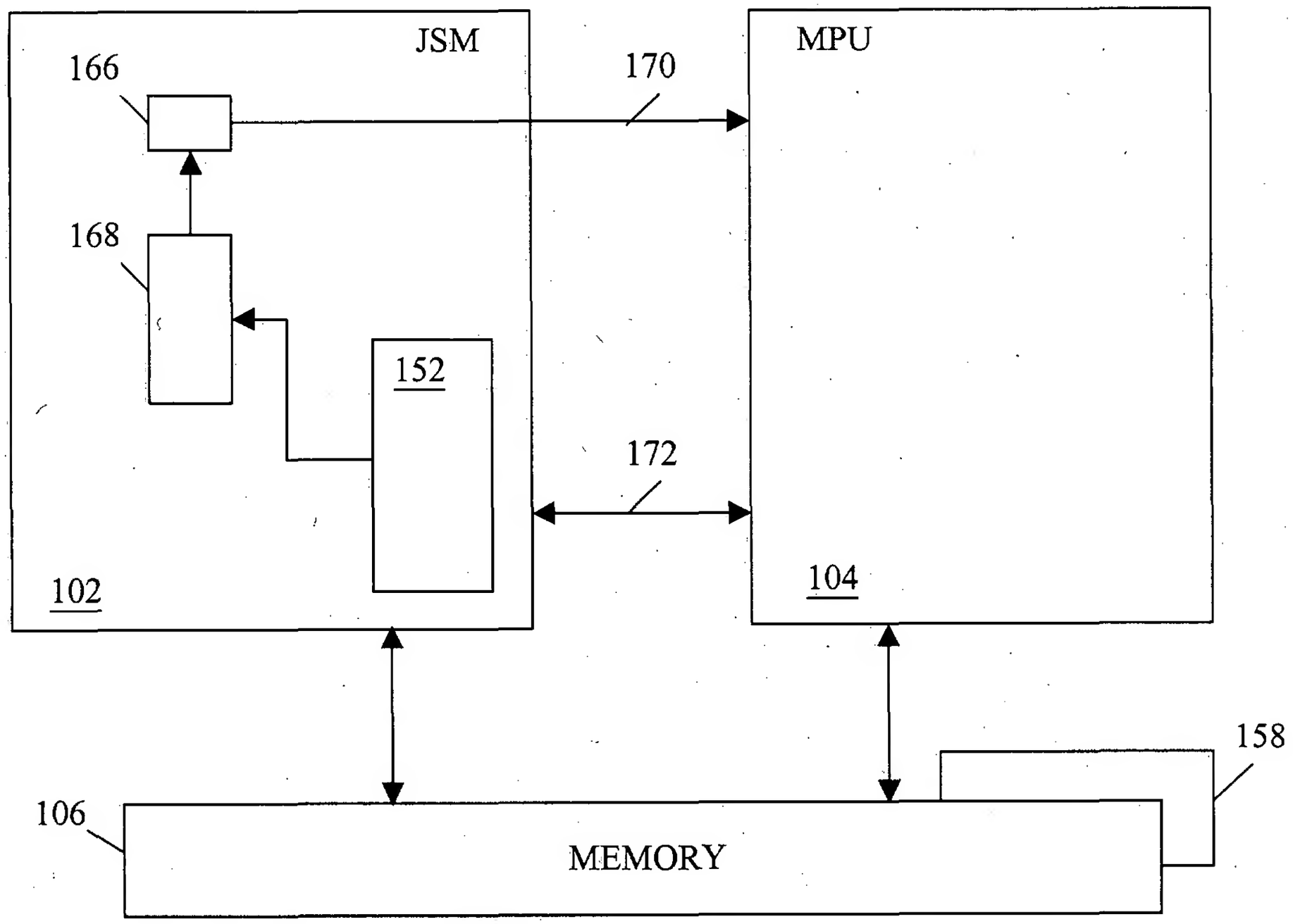

Figure 4